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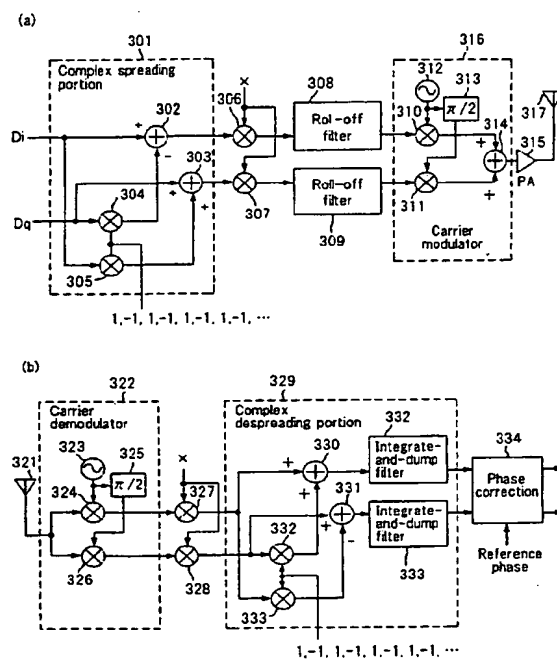
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(54) SPREAD-SPECTRUM COMMUNICATION DEVICE

(57) A spread spectrum communication system is to control the signal amplitude variation so as to alleviate the requirement of the linearity on the amplifier etc., thus allowing for use of a compact, low cost and energy saving transmitter.

The transmission signal is separated into the I-phase component and the Q-phase component. In a complex spreading portion (301), spreading is performed by using multipliers (304 and 305) and adds (302 and 303) together with a sequence pattern of 1 and -1 appearing alternately. The outputs from the complex spreading portion (301) are modulated in multipliers (306 and 307) using pseudo-random sequences PN^(k)(x) allotted for individual users. The baseband signal which underwent waveform shaping by roll-off filters (308 and 309) is modulated through a carrier modulator (316), then sent to a power amplifier (315), where it is amplified and transmitted via antenna (317).

FIG. 3



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Description

Technical Field

[0001] The present invention relates to a spread spectrum communication system based on the direct sequence spreading scheme, for a portable phone and mobile communication system, for example.

Background Art

[0002] In recent years, research and development as to mobile communication systems using the spread spectrum technology based on the direct sequence spreading scheme have been extensively made. Such communication systems include a system used worldwide and known as cdmaOne, and the W-CDMA system and the Wideband cdmaOne(cdma2000) system which both are aiming at the standard for IMT-2000 (see Tero Ojanperä and Ramjee Prasad, "An Overview of Air Interface Multiple Access for IMT-2000/UMTS", IEEE Communications Magazine, September 1998. etc.). Among these, particularly for the W-CDMA, use of roll-off filters having small roll-off ratios is demanded in order to reduce the required bandwidth as small as possible relative to the chip rate. As the spread spectrum communication system for this scheme, a method combined with QPSK(quadri-phase shift keying) has been proposed.

[0003] Fig.1 shows a block diagram of a transmitter of this spread spectrum communication system of the QPSK scheme. The transmitter of this spread spectrum communication system is comprised of a complex spreading portion 701 for performing complex spreading modulation of data D_i and D_q which are the I-phase component(in-phase component) and the Q-phase component(quadrature-phase component) of the transmission signal, roll-off filters 708 and 709, a carrier modulator 716, a power amplifier 715 and an antenna 717.

[0004] Complex spreading portion 701 includes multipliers 702 to 705 for multiplying the I-phase and Q-phase data by complex number sequences W_0 and W_1 and adders 706 and 707 each performing addition of the I-phase and Q-phase data modulated by W_0 and W_1 .

[0005] Carrier modulator 716 includes a sinusoidal wave generating circuit 710, a phase-rotation circuit 713 for rotating the phase of the sinusoidal wave by $\pi/2$, a multiplier 711 for modulating the data having passed through roll-off filter 708 by the sinusoidal wave and a multiplier 712 for modulating the data having passed through roll-off filter 709 by the sinusoidal wave rotated in phase by $\pi/2$. and an adder 714 for performing addition of these pieces of modulated data.

[0006] However, since the signal waveforms having passed through the roll-off filters present large dynamic ranges, a spread spectrum communication system of this type may be demanded high linearity for a power amplifier and other circuits.

[0007] To deal with the problem of this kind, Japanese Patent Publication Hei 7 No.312391 discloses a spread spectrum transmitter/receiver in which spectrum spreading and a $\pi/4$ shift QPSK are applied in combination. In this $\pi/4$ shift QPSK, since the positions of the signal points of sequential symbols differ in phase by $\pi/4$ from each other, the envelop will not cut the point '0' when the signal transits from one signal point to another, so that the variations of the envelop is known to be small.

This effect can be similarly obtained when it is applied to a spread spectrum scheme.

[0008] On the other hand, as found in 'Global CDMA II for IMT-2000 RTT System Description' TTA, Korea (June 17, 1998), a special spreading scheme based on the combination of pseudo-random sequence and Walsh sequence has been also proposed. Now, this scheme is called OCQPSK(orthogonal complex QPSK) and the block diagram is shown in Fig.2.

[0009] The transmitter for the spread spectrum communication system based on this OCQPSK scheme is comprised of multipliers 802 and 803 for modulating the I-phase and Q-phase data D_i and D_q by Walsh sequences W_0 and W_2 , respectively, a complex spreading portion 801 for performing complex spreading modulation, multipliers 810 and 811 for scrambling the data with pseudo-random sequences $PN^{(k)}$, roll-off filters 812 and 813, a carrier modulator 820, a power amplifier 819 and an antenna 821.

[0010] Complex spreading portion 801 includes multipliers 804 to 807 for multiplying the I-phase and Q-phase data modulated by W_0 and W_2 by complex number sequences W_0 and W_1 , and adders 808 and 809 each performing addition of the I-phase and Q-phase data modulated by W_0 and W_1 .

[0011] Carrier modulator 820 includes a sinusoidal wave generating circuit 814, a phase-rotation circuit 817 for rotating the phase of the sinusoidal wave by $\pi/2$, a multiplier 815 for modulating the data having passed through roll-off filter 812 by the sinusoidal wave and a multiplier 816 for modulating the data having passed through roll-off filter 813 by the sinusoidal wave rotated in phase by $\pi/2$, and an adder 818 for performing addition of these pieces of modulated data.

[0012] In this spread spectrum communication system, the I-phase data D_i and Q-phase data D_q are modulated by Walsh sequences W_0 and W_2 and further processed through complex spreading portion 801 where the signals are subjected to complex spreading modulation using complex number sequences made up of W_0 and W_1 . Further, the output signals are scrambled by a pseudo-random sequence $PN^{(k)}$ uniquely allotted to the user. The signals are then passed through the roll-off filters and are processed through carrier modulating portion 820, where the signals are carrier-modulated to be output to power amplifier 819. This method has the feature of limiting the phase transition of the signal to reduce the amplitude variation of the signal by performing spread modulation using complex number sequences.

es made up of Walsh sequences.

[0013] The method disclosed by Japanese Patent Publication Hei 7 No.312391 is able to improve the characteristic compared to the QPSK spreading modulation, however, the degree of the improvement is not so large and hence is not large enough to meet the conditions required by W-CDMA.

[0014] The OCQPSK scheme is able to provide a certain degree of improvement, however, this is not good enough. That is, the reason why the OCQPSK cannot produce high enough improvement is that the Q-phase data D_q is spread modulated by codes represented by W_2 or that the signal is varied in phase by units of 90 degrees every two chips when the signal is input to complex spreading portion 801 shown in Fig.2. Since the restriction on phase transition is effective only when the phase of the symbol does not vary, the effect of restriction on phase transition is limited to one of two transitions, thus sharp improvement of the characteristics cannot be obtained.

[0015] It is therefore an object of the present invention to provide a spread spectrum communication system in which the requirement on the linearity for the amplifier etc. can be alleviated by inhibiting the amplitude variations of the signal more tightly than the above schemes, thus allowing for use of a compact, low-cost and energy saving transmitter.

Disclosure of Invention

[0016] In order to solve the above problem, the present invention is configured as follows:

[0017] The first aspect of the invention is a spread spectrum communication system including a transmitter and receiver for performing spread spectrum communications based on a direct sequence spreading scheme and is characterized in that the transmitter includes: a complex spread portion for multiplying an I-phase component signal and a Q-phase component signal of the transmission signal by one type of complex number sequence which will not cause any phase transition of a signal on the I-Q plane in the direction toward the origin; a multiplier for multiplying the signals output from the complex spreading portion by a pseudo-random sequence which is generated at a speed exceeding the symbol rate; a roll-off filter for waveform shaping; and a carrier modulator for performing carrier modulation of the signals having undergone waveform shaping, and that the receiver includes: a carrier demodulator for performing carrier demodulation of the received signal; a multiplier for multiplying the two types of signals output from the carrier demodulator by the pseudo-random sequence generated at the same speed as above; a complex despreading portion for performing despreading by multiplying each signal by the complex number sequence; and a phase-correcting portion for performing phase-correction so as to extract the I-phase and Q-phase components.

[0018] The second aspect of the invention is the spectrum spread communication system defined in the first aspect and is characterized in that the complex spreading portion includes: a multiplier for multiplying the I-phase component signal and Q-phase component signal of the transmission signal by the complex number sequence; and an adder for performing addition of the I-phase component signal and Q-phase component signal of the transmission signal respectively to the Q-phase component signal and I-phase component signal multiplied by the complex number sequence, and that the complex despreading portion wherein the complex despreading portion performs the despreading includes: a multiplier for multiplying the signals by the complex number sequence; and an adder for performing addition of the signals to the signals multiplied by the complex number sequence, respectively.

[0019] The third aspect of the invention is the spectrum spread communication system defined in the first or second aspect, and is characterized in that the complex number sequence is a pattern in which the I-phase component is constantly set at 1 or -1 and the Q-phase component changes between 1 and -1 alternately.

[0020] The fourth aspect of the invention is the spectrum spread communication system defined in any of the first through third aspects, and further includes: a mapping circuit disposed prior to the transmitter for mapping the multiplexed transmission signals to points on the I-Q plane.

[0021] The fifth aspect of the invention is a spread spectrum communication system including a transmitter and receiver for performing spread spectrum communications based on a direct sequence spreading scheme, and is characterized in that the transmitter includes: a permuting processor for permuting the I-phase component signal and the Q-phase component signal of the transmission signal, once every two clock units and at the same time inverting the sign of one of the component signals; a multiplier for multiplying the signals output from the complex spreading portion by a pseudo-random sequence which is generated at a speed exceeding the symbol rate; a roll-off filter for waveform shaping; and a carrier modulator for performing carrier modulation of the signals having undergone waveform shaping, and that the receiver includes: a carrier demodulator for performing carrier demodulation of the received signal; a multiplier for multiplying the two types of signals output from the carrier demodulator by the pseudo-random sequence generated at the same speed as above; a permuting processor for permuting the signal multiplied by the pseudo-random sequence, once every two clock units and at the same time inverting the sign of the component signal which underwent sign inversion at the transmitter; and a phase-correcting portion for performing phase-correction so as to extract the I-phase and Q-phase components.

[0022] The sixth aspect of the invention is the spectrum spread communication system defined in the fifth

aspect, and is characterized in that the permuting processor includes: a multiplier for multiplying one of the component signals of the transmission signal by -1; and a switch which, based on a control signal of 1 and 0 appearing alternately, switches between the combination of the I-phase component signal and Q-phase component signal of the transmission signal and the combination of one component signal multiplied by -1 and the other component signal, and that the permuting inverse processor includes: a multiplier for multiplying the signal which was multiplied by the pseudo-random sequence by -1; a switch which, based on a control signal of 1 and 0 appearing alternately, switches between the combination of the signals which were multiplied by a pseudo-random sequence and the combination between the signal multiplied by -1 and the other signal multiplied by another pseudo-random sequence.

[0023] The seventh aspect of the invention is that the spectrum spread communication system defined in the fifth or sixth aspect, and further includes: a mapping circuit disposed prior to the transmitter for mapping the multiplexed transmission signals to points on the I-Q plane.

[0024] The eighth aspect of the invention is the spectrum spread communication system defined in the seventh aspect, and is characterized in that the mapping circuit maps each of the signals to the I-phase and Q-phase and independently sets the I-phase or Q-phase amplitude and symbol rate, if required.

[0025] The ninth aspect of the invention, the spectrum spread communication system defined in the seventh aspect, and is characterized in that the mapping circuit has the mapping function of mapping, when a multiple number of data channels are needed to be allotted in response to an information transfer request arising regularly or eventually, the data onto the I-Q plane by using a multiple number of orthogonal sequences whereby increase in symbol rate due to mapping is minimized.

Brief Description of the Drawings

[0026]

Fig.1 is a block diagram showing a transmitter in a conventional QPSK spread spectrum communication system;

Fig.2 is a block diagram showing a transmitter in a conventional OCQPSK spread spectrum communication system;

Fig.3 is a block diagram showing the first embodiment of a spread spectrum communication system in accordance with the present invention, (a) and (b) being a transmitter and receiver, respectively;

Fig.4 shows characteristic charts representing time-dependent variations in amplitude of the signals having passed through roll-off filters, (a) for a typical QPSK system, (b) for OCQPSK system and (c) for a system of the first embodiment;

Fig.5 shows characteristic charts representing the cumulative probability distribution of the instantaneous value of the roll-off filter output value, (a) representing the peak value distributions in the above systems and (b) representing the minimum value distributions;

Fig.6 is a block diagram showing the second embodiment of a spread spectrum communication system in accordance with the present invention;

Fig.7 shows illustrative diagrams representing the allocations of the signal points according to the first and second embodiments; and

Fig.8 is a block diagram showing the third embodiment of a spread spectrum communication system in accordance with the present invention.

Best Mode for Carrying Out the Invention

[0027] Next, the embodiments of the present invention will be described with reference to the drawings.

<The First Embodiment>

[0028] Fig. 3 is a block diagram showing the first embodiment of a spread spectrum communication system in accordance with the present invention, Fig.3(a) and Fig.3(b) showing a transmitter and receiver, respectively.

[0029] As shown in Fig.3(a), the transmitter is comprised of a complex spreading portion 301 for performing complex spreading modulation of the I-phase and Q-phase data D_i and D_q of a transmission signal, multipliers 306 and 307 for scrambling the data with a pseudo-random sequence $PN^{(k)}$ which is generated at a speed equal to or above some times or some hundreds times of the symbol rate, roll-off filters 308 and 309 for waveform shaping, a carrier modulator 316, a power amplifier 315 and an antenna 317.

[0030] Complex spreading portion 301 includes a multiplier 305 for multiplying the I-phase data D_i by a complex number sequence of 1 and -1 appearing alternately, a multiplier 304 for multiplying the Q-phase data D_q by the above complex number sequence, an adder 302 for adding the modulated data through multiplier 304 to the I-phase data D_i and an adder 303 for adding the modulated data through multiplier 305 to the Q-phase data D_q .

[0031] The data output from adder 302 is supplied to multiplier 306 and the data from adder 303 is supplied to multiplier 307.

[0032] Carrier modulator 316 includes a sinusoidal wave generating circuit 312, a phase-rotation circuit 313 for rotating the sinusoidal wave in phase by $\pi/2$ at the same speed as that at which pseudo-random sequence $PN^{(k)}$ is generated, a multiplier 310 for modulating the data having passed through roll-off filter 308 by the sinusoidal wave and a multiplier 311 for modulating the data having passed through roll-off filter 309 by the si-

sinusoidal wave rotated in phase by $\pi/2$, and an adder 314 for performing addition of these modulated data.

[0033] First, the transmission signal is separated into the I-phase component and the Q-phase component, which are input to complex spreading portion 301. In complex spreading portion 301, the data is spread by a complex number sequence pattern of 1 and -1 appearing alternately through multipliers 304 and 305 and adders 302 and 303. Therefore, if no change occurs in data, the signal outputs from complex spreading portion 301 continuously change in phase by ± 90 degrees.

[0034] The output from complex spreading portion 301 is modulated by pseudo-random sequences $PN^{(k)}$ (x) allotted to each user through multipliers 306 and 307. Then, the signal is wave-shaped through roll-off filters 308 and 309 so that the baseband signal is modulated by carrier modulator 316, so that the resulting signal is supplied to power amplifier 315, where it is amplified and transmitted from antenna 317.

[0035] As shown in Fig.3(b), the receiver is comprised of an antenna 321, a carrier demodulator 322, multipliers 327 and 328 for demodulating the signal with pseudo-random sequence $PN^{(k)}(x)$, a complex despread portion 329 for performing complex despread and a phase rotating circuit 334 for performing phase correction based on the reference phase.

[0036] Carrier demodulator 322 includes a sinusoidal wave generating circuit 323, a phase rotating circuit 325 for rotating the sinusoidal wave in phase by $\pi/2$, a multiplier 324 for demodulating the received signal through antenna 321 by the sinusoidal wave and a multiplier 326 for demodulating the received signal through antenna 321 by the sinusoidal wave rotated in phase by $\pi/2$.

[0037] Multiplier 324 outputs the demodulated signal to multiplier 327.

[0038] Multiplier 326 outputs the demodulated signal to multiplier 328.

[0039] Complex despread portion 329 includes a multiplier 333 for multiplying the output signal from multiplier 327 by the complex number sequence, a multiplier 332 for multiplying the signal output from multiplier 328 by the complex number sequence, an adder 330 for adding the output signal from multiplier 332 to the signal output from multiplier 327, an adder 331 for adding the output signal from multiplier 333 to the signal output from multiplier 328 and integrate-and-dump filters 332 and 333 for performing waveform shaping of the signals received from adders 330 and 331.

[0040] On the receiver side, the signal after the demodulation through carrier demodulator 322 is multiplied with x, i.e., pseudo-random sequence $PN^{(k)}$, through multipliers 327 and 328. Then the signal is further despread through complex despread portion 329 and is corrected in phase by phase rotating circuit 334 based on the reference phase information determined using a pilot symbol etc., whereby I-phase and Q-phase information is extracted.

[0041] In this spread spectrum communication sys-

tem, the data is spread using the complex number sequence pattern of 1 and -1 appearing alternately, instead of performing modulation by W_0 and W_2 prior to complex spreading portion 801 shown in Fig. 2, so that the signal output from complex spreading portion 301 will continuously change in phase by ± 90 degrees as long as the data is constant. In this way, the signal input to complex spreading portion 301 is prevented to change at a rate greater than the symbol rate, whereby the effect of phase transition restriction can be enhanced. Thus, phase transition of the spread signal is restricted so as to reduce the amplitude variations of the signal, which makes it possible to alleviate the linearity requirement on the amplifier. An example of the time-dependent variation in amplitude of the signal having passed through the root roll-off filter having a roll-off factor of 0.22 is shown in Fig.4.

[0042] The chart shown in Fig.4(a) represents the characteristic of the amplitude variation of a signal which is spread modulated through a typical QPSK spread system and then passed through the root roll-off filter. In contrast, the Fig.4(b) represents the case using the OCQPSK scheme and Fig.4(c) represents the amplitude variation when the first embodiment is applied. The amplitude variation range of the OCQPSK scheme turned out to be smaller than that of the QPSK scheme. The first embodiment presented a further smaller variation range, proving improvement.

[0043] Fig.5 shows the cumulative probability distribution of the instantaneous value of the roll-off filter output value. Fig.5(a) represents the peak value distributions of the above systems and Fig.5(b) represents the minimum value distributions. Since the present embodiment shows the lowest peak value and the largest minimum, it is understood that the amplitude variation range is the smallest compared to the other systems.

<The Second Embodiment>

[0044] Fig.6 is a block diagram showing the second embodiment of a spread spectrum communication system in accordance with the present invention. This spread spectrum communication system represents its transmitter, which comprised of a multiplier 401 for multiplying the I-phase data D_i by -1 and a switching circuit 402, instead of the complex spreading portion 301 of the first embodiment. Other components are the same as in the first embodiment so the same reference numerals as in the first embodiment are allotted. Switching circuit 402 has switches 403 and 404 controlled in accordance with a control signal.

[0045] The transmission signal is separated into the I-phase and Q-phase components and input to switching circuit 402. The I-phase component is processed through multiplier 401 so as to continuously generate its negative-phase signal, which is also supplied to switching circuit 402.

[0046] Two switches 403 and 404 in switching circuit

402 are adapted to operate in accordance with a control signal. When a signal which alternately changes between 0 and 1 is supplied as the control signal, the inputs are directly output for even-numbered chips and the inputs D_i and D_q are permuted for odd-numbered chips. For the latter case, the sign of data D_i is inverted by multiplier 401 and input. The output from switching circuit 402 is always shifted in phase by 45 degrees relative that of the first embodiment (see Fig. 7). Also in this case, the signal continuously changes in phase by 90 degrees if no change occurs in data, so that the same effect as in the first embodiment can be obtained.

[0047] The other operation is the same as the first embodiment. Here, no receiver is shown but the switching circuit is configured so as to perform reverse operations of the transmitter and the same configuration as in the first embodiment can be employed for the other parts.

<The third embodiment>

[0048] Fig. 8 is a block diagram showing the third embodiment of a spread spectrum communication system in accordance with the present invention, in which a mapping circuit to be disposed prior to the transmitter shown in Fig. 3(a) or Fig. 6 is shown. In the first and second embodiments, data is allotted for I-phase and Q-phase components as transmission data D_i and D_q . However, for multimedia communications to which W-CDMA is expected to be applied, in order to flexibly deal with various data rates, multi-rate transmission, multi-code transmission and the like are desired.

[0049] Fig. 8 shows a mapping circuit 601 for generating the symbol which is the combination of parallel input data $D^{(0)}$, $D^{(1)}$, $D^{(2)}$, ..., $D^{(N-1)}$ and input to the transmitter in Fig. 3(a) or Fig. 6.

[0050] Mapping circuit 601 is configured of multiple multipliers 603 to 604, 606 to 608, ..., multiple adders 605, 609, ..., and circuits for generating orthogonal codes C_n . Here, a value 2^1 satisfying the condition $2^{1-1} < [N/2] \leq 2^1$ is determined, where $[x]$ denotes a minimum integer equal to or greater than x .

[0051] Under this condition, the symbol rate of the output from mapping circuit 601 becomes equal to 2^1 times of the largest symbol rate of the input to mapping circuit 601. For symbol mapping, arbitrary orthogonal codes can be used. As an example, Walsh-Hadamard sequences can be used. Now, a specific example will be shown. $D^{(0)}$ is assumed as a control channel and $D^{(1)}$, $D^{(2)}$ and $D^{(3)}$ are assumed as data channels. The symbol rate of the control channel is 1/256 of the chip rate and the data channel is 1/32.

[0052] In this case, it is assumed that $C_0 = 1$, 1 and $C_1 = 1$, -1 , so that the outputs from mapping circuit 601 are obtained as follows:

$$D_i = D^{(0)} \cdot C_0 + D^{(2)} \cdot C_1$$

$$D_q = D^{(1)} \cdot C_0 + D^{(3)} \cdot C_1$$

For this case, the clock for C_0 and C_1 becomes equal to twice of that of $D^{(1)}$, $D^{(2)}$ and $D^{(3)}$. Thus, one control channel and three data channels can be mapped to generate a symbol having a speed one-sixteenth of the chip rate. This symbol is input to the circuit shown in Fig. 3(a) or Fig. 6 and is transmitted. In this way, use of such a mapping circuit makes it possible for a single transmitter to send data involving a multiple pieces of information different in transmission rate and in required quality. The information can be demodulated using one or multiple receivers.

[0053] According to the present invention, the signals (information symbols arranged at symbol points) arranged on the I-Q plane are subjected to spectrum spreading using complex number sequences devised so as not to cause phase-transition in a diagonal line direction (toward the origin point), whereby it is possible to reduce the amplitude variation of the signal after roll-off filters and hence narrow the signal dynamic range, which allows a relatively low-cost high efficiency amplifier to satisfy the system specifications as to adjacent channel interface etc. As a result, this invention contributes to reducing the cost of the system and to reducing the power consumption.

[0054] Further, use of a mapping circuit makes it possible for a single transmitter to send data involving a multiple pieces of information different in transmission rate and in required quality.

Industrial Applicability

[0055] As has been described heretofore, the spread spectrum communication system according to the present invention is applicable to spread spectrum communication systems of a direct sequence spreading type which is used for portable phones, mobile communication systems and the like. Further, the invention contributes to miniaturization, power saving and cost reduction of the transmitter.

Claims

1. A spread spectrum communication system including a transmitter and receiver for performing spread spectrum communications based on a direct sequence spreading scheme, the transmitter comprising:

a complex spread portion for multiplying an I-phase component signal and a Q-phase component signal of the transmission signal by one type of complex number sequence which will not cause any phase transition of a signal on the I-Q plane in the direction toward the origin;

a multiplier for multiplying the signals output from the complex spreading portion by a pseudo-random sequence which is generated at a speed exceeding the symbol rate; a roll-off filter for waveform shaping; and a carrier modulator for performing carrier modulation of the signals having undergone waveform shaping, the receiver comprising:

a carrier demodulator for performing carrier demodulation of the received signal; a multiplier for multiplying the two types of signals output from the carrier demodulator by the pseudo-random sequence generated at the same speed as above; a complex despreading portion for performing despreading by multiplying each signal by the complex number sequence; and a phase-correcting portion for performing phase-correction so as to extract the I-phase and Q-phase components.

2. The spectrum spread communication system according to Claim 1, wherein the complex spreading portion includes:

a multiplier for multiplying the I-phase component signal and Q-phase component signal of the transmission signal by the complex number sequence, and an adder for performing addition of the I-phase component signal and Q-phase component signal of the transmission signal respectively to the Q-phase component signal and I-phase component signal multiplied by the complex number sequence; and

the complex despreading portion wherein the complex despreading portion performs the despreading includes:

a multiplier for multiplying the signals by the complex number sequence, and an adder for performing addition of the signals to the signals multiplied by the complex number sequence, respectively.

3. The spectrum spread communication system according to Claim 1 or 2, wherein the complex number sequence is a pattern in which the I-phase component is constantly set at 1 or -1 and the Q-phase component changes between 1 and -1 alternately.
4. The spectrum spread communication system according to any one of Claims 1 through 3, further comprising:

a mapping circuit disposed prior to the transmitter for mapping the multiplexed transmission signals to points on the I-Q plane.

5. A spread spectrum communication system including a transmitter and receiver for performing spread spectrum communications based on a direct sequence spreading scheme, the transmitter comprising:

a permuting processor for permuting the I-phase component signal and the Q-phase component signal of the transmission signal, once every two clock units and at the same time inverting the sign of one of the component signals; a multiplier for multiplying the signals output from the complex spreading portion by a pseudo-random sequence which is generated at a speed exceeding the symbol rate; a roll-off filter for waveform shaping; and a carrier modulator for performing carrier modulation of the signals having undergone waveform shaping,

the receiver comprising:

a carrier demodulator for performing carrier demodulation of the received signal; a multiplier for multiplying the two types of signals output from the carrier demodulator by the pseudo-random sequence generated at the same speed as above; a permuting processor for permuting the I-phase component signal multiplied by the pseudo-random sequence, once every two clock units and at the same time inverting the sign of the component signal which underwent sign inversion at the transmitter; and a phase-correcting portion for performing phase-correction so as to extract the I-phase and Q-phase components.

6. The spectrum spread communication system according to Claim 5, wherein the permuting processor includes:

a multiplier for multiplying one of the component signals of the transmission signal by -1; and a switch which, based on a control signal of 1 and 0 appearing alternately, switches between the combination of the I-phase component signal and Q-phase component signal of the transmission signal and the combination of one component signal multiplied by -1 and the other component signal,

the permuting inverse processor includes:

a multiplier for multiplying the signal which was multiplied by the pseudo-random sequence by -1;

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a switch which, based on a control signal of 1 and 0 appearing alternately, switches between the combination of the signals which were multiplied by a pseudo-random sequence and the combination between the signal multiplied by -1 and the other signal multiplied by another pseudo-random sequence.

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7. The spectrum spread communication system according to Claim 5 or 6, further comprising:

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a mapping circuit disposed prior to the transmitter for mapping the multiplexed transmission signals to points on the I-Q plane.

8. The spectrum spread communication system according to Claim 7, wherein the mapping circuit maps each of the signals to the I-phase and Q-phase and independently sets the I-phase or Q-phase amplitude and symbol rate, if required.

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9. The spectrum spread communication system according to Claim 7, wherein the mapping circuit has the mapping function of mapping, when a multiple number of data channels are needed to be allotted in response to an information transfer request arising regularly or eventually, the data onto the I-Q plane by using a multiple number of orthogonal sequences whereby increase in symbol rate due to mapping is minimized.

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FIG. 1

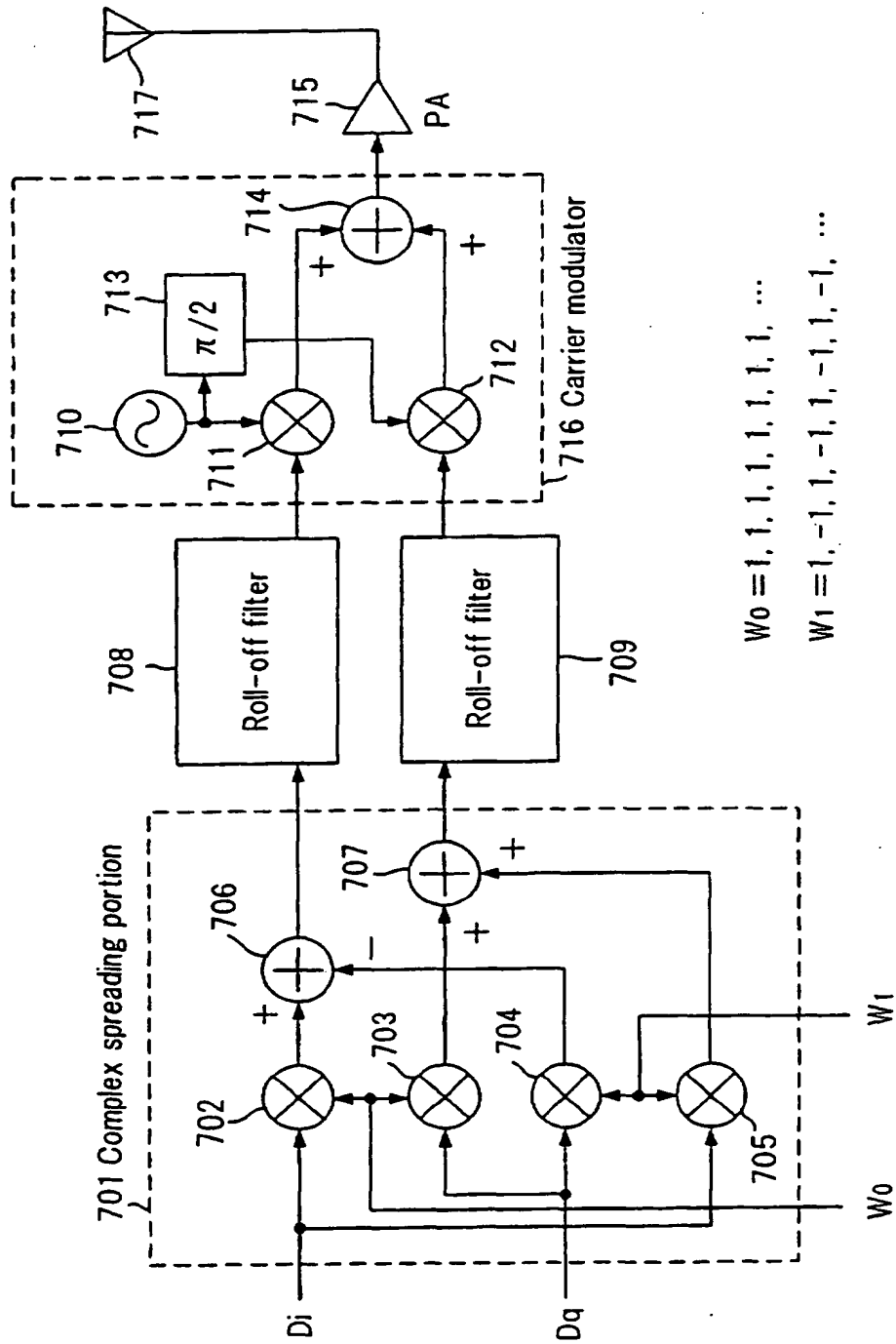


FIG. 2

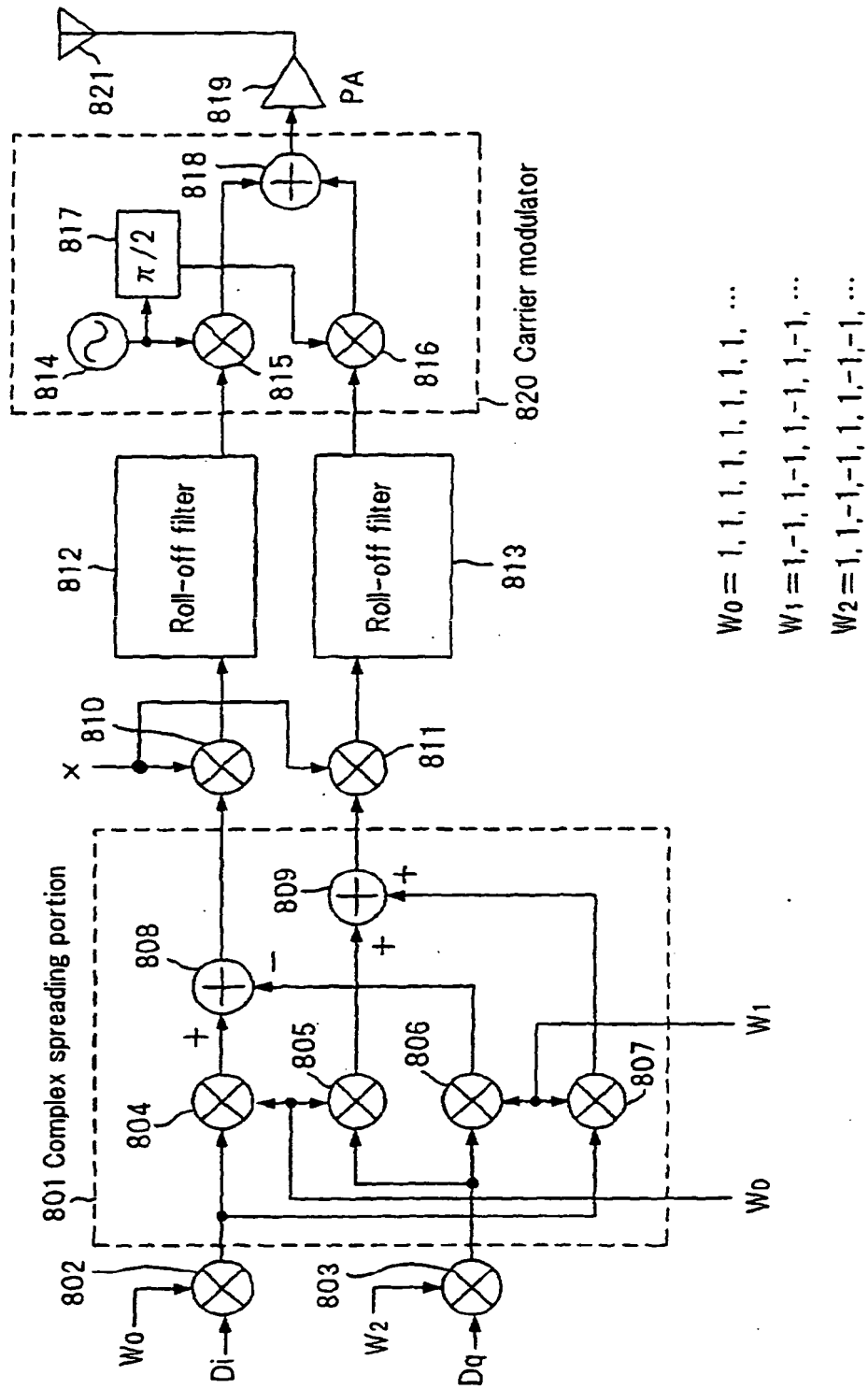


FIG. 3

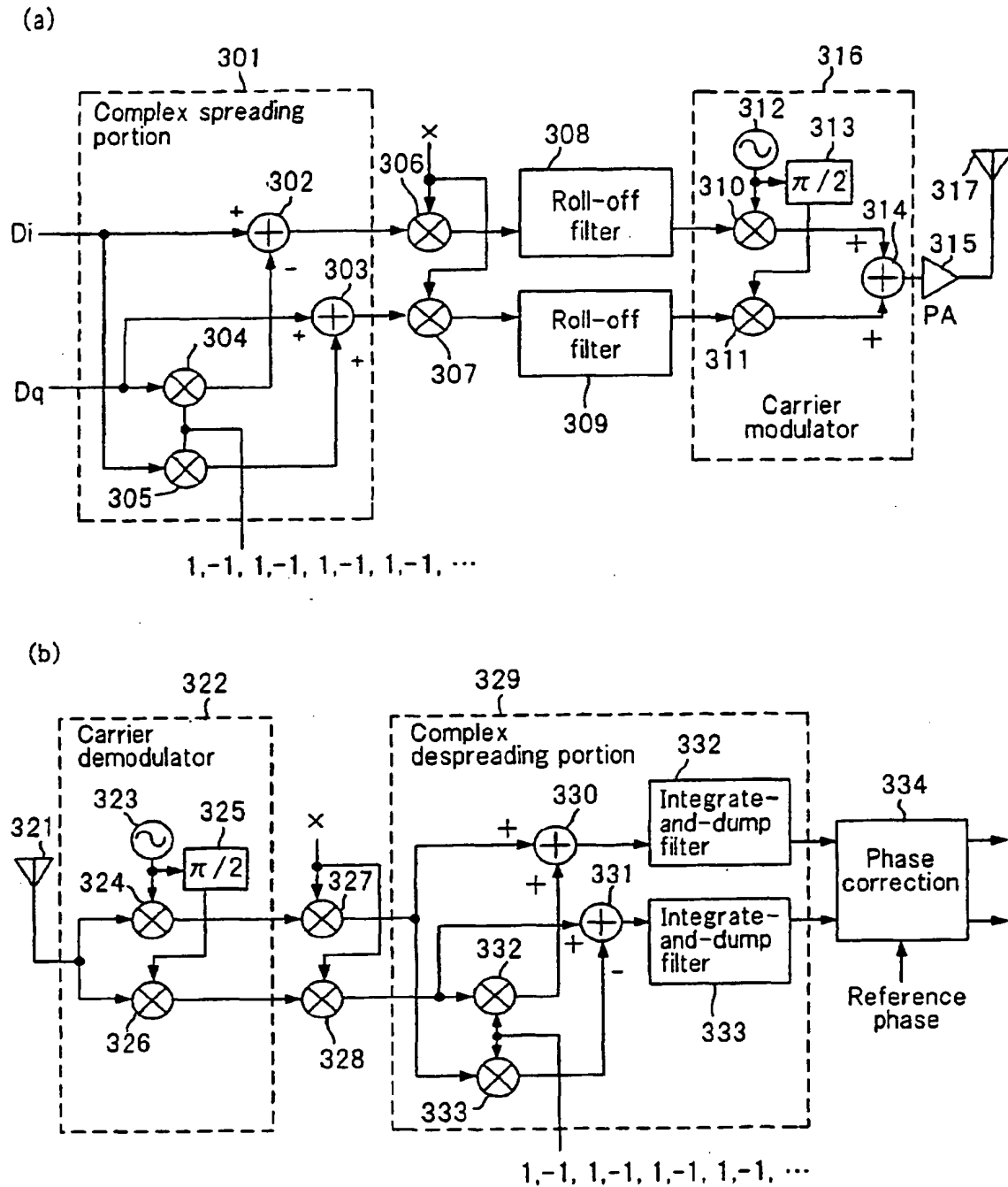


FIG. 4

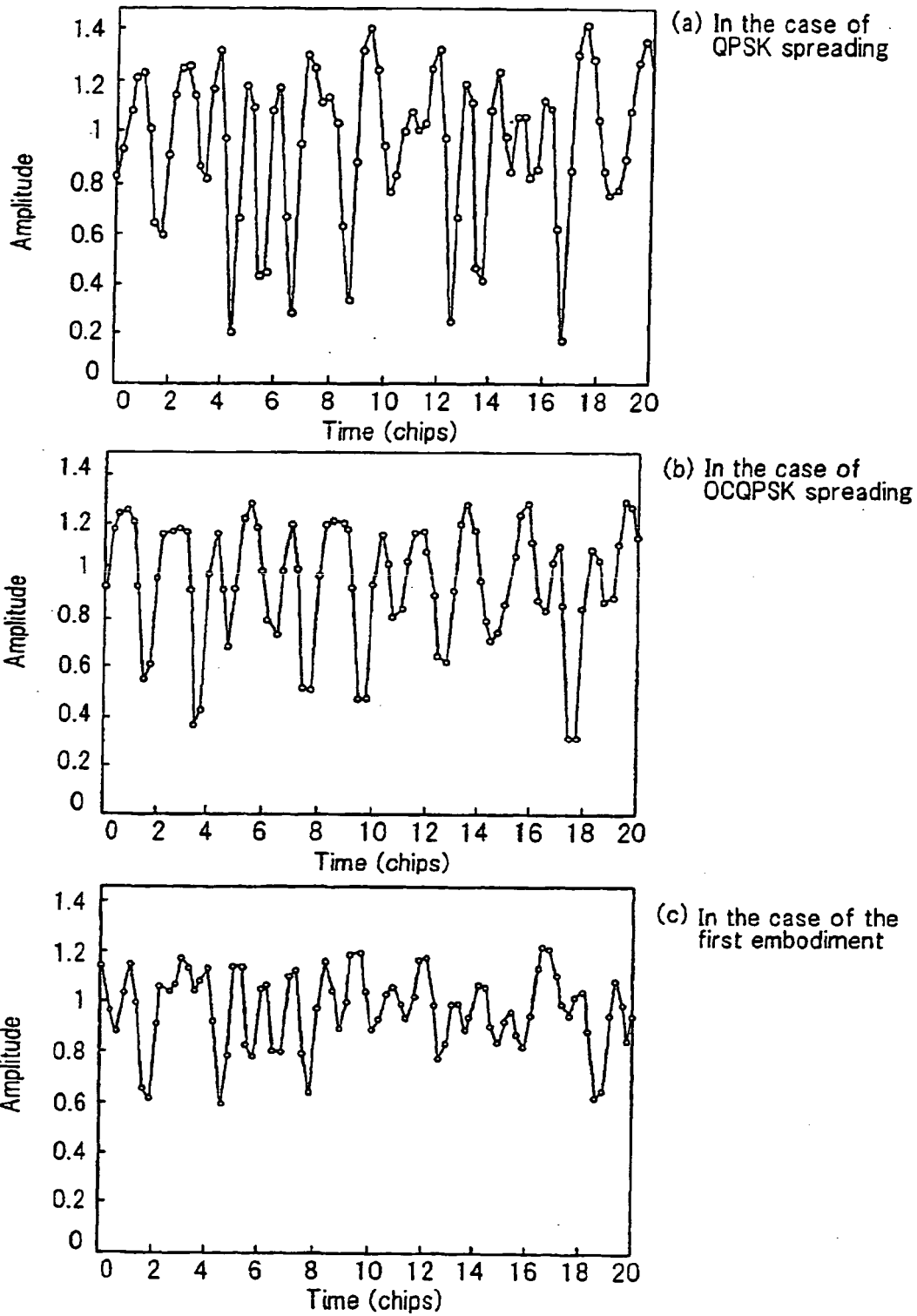


FIG.5

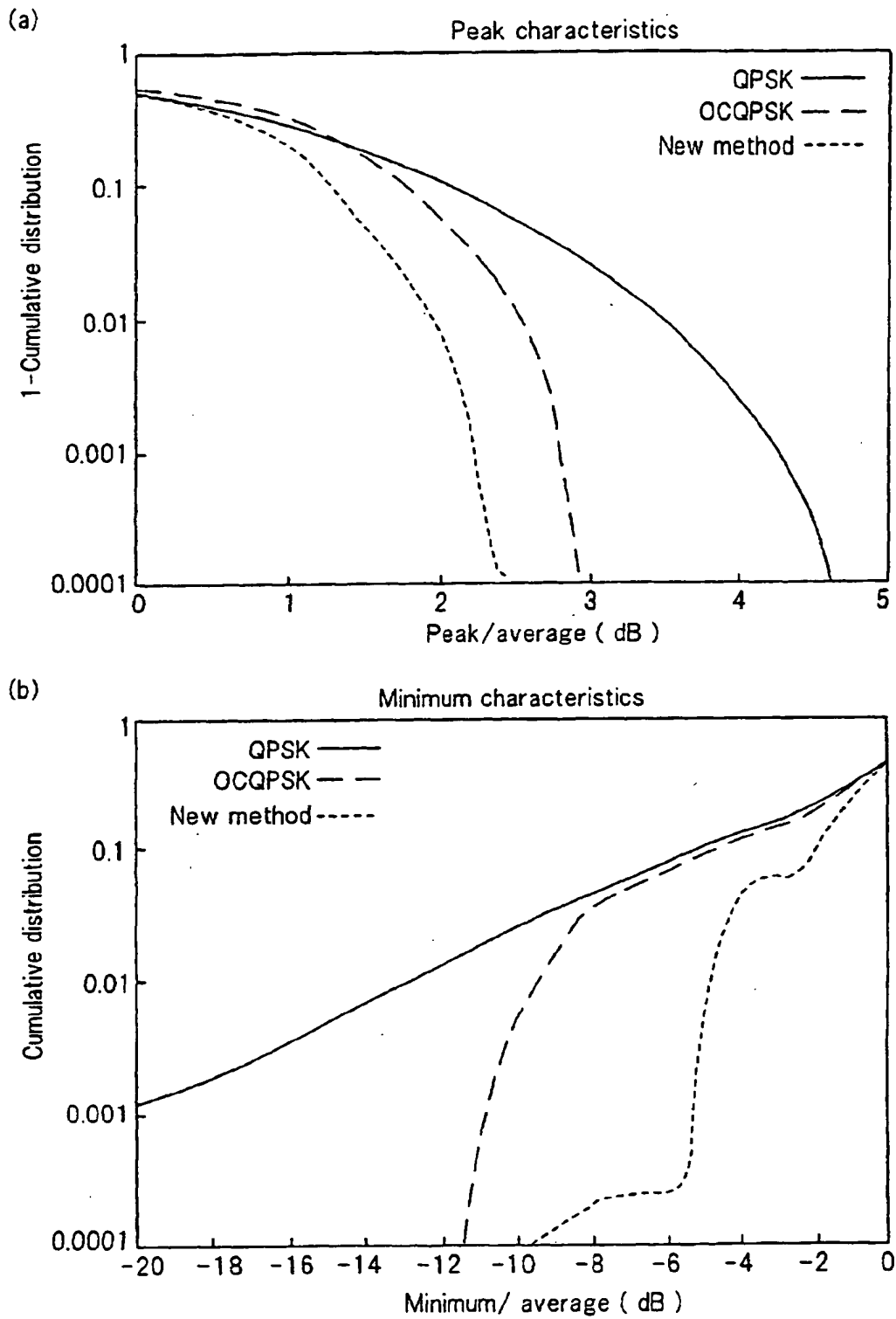


FIG. 6

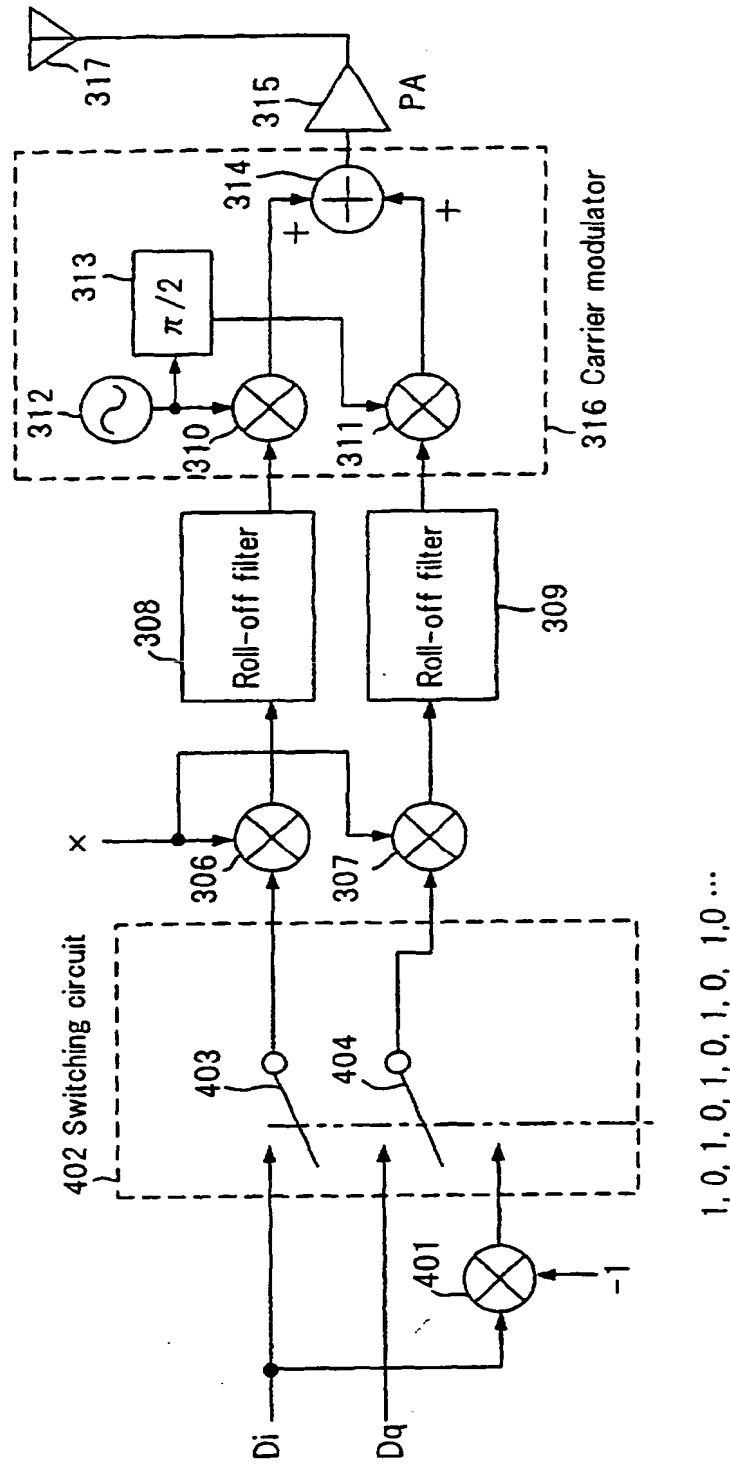
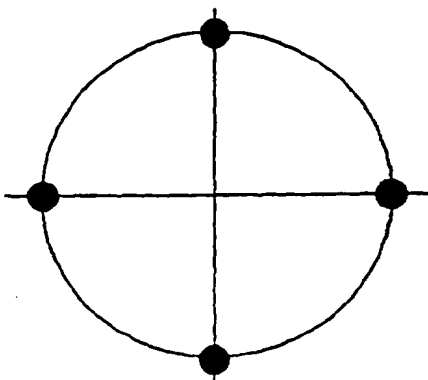
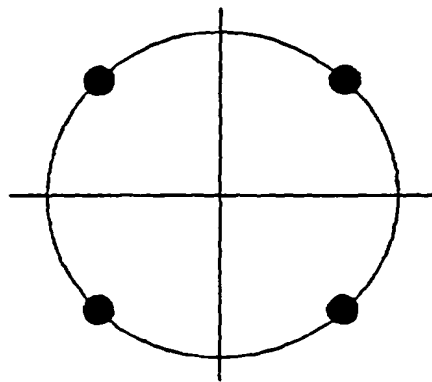


FIG. 7

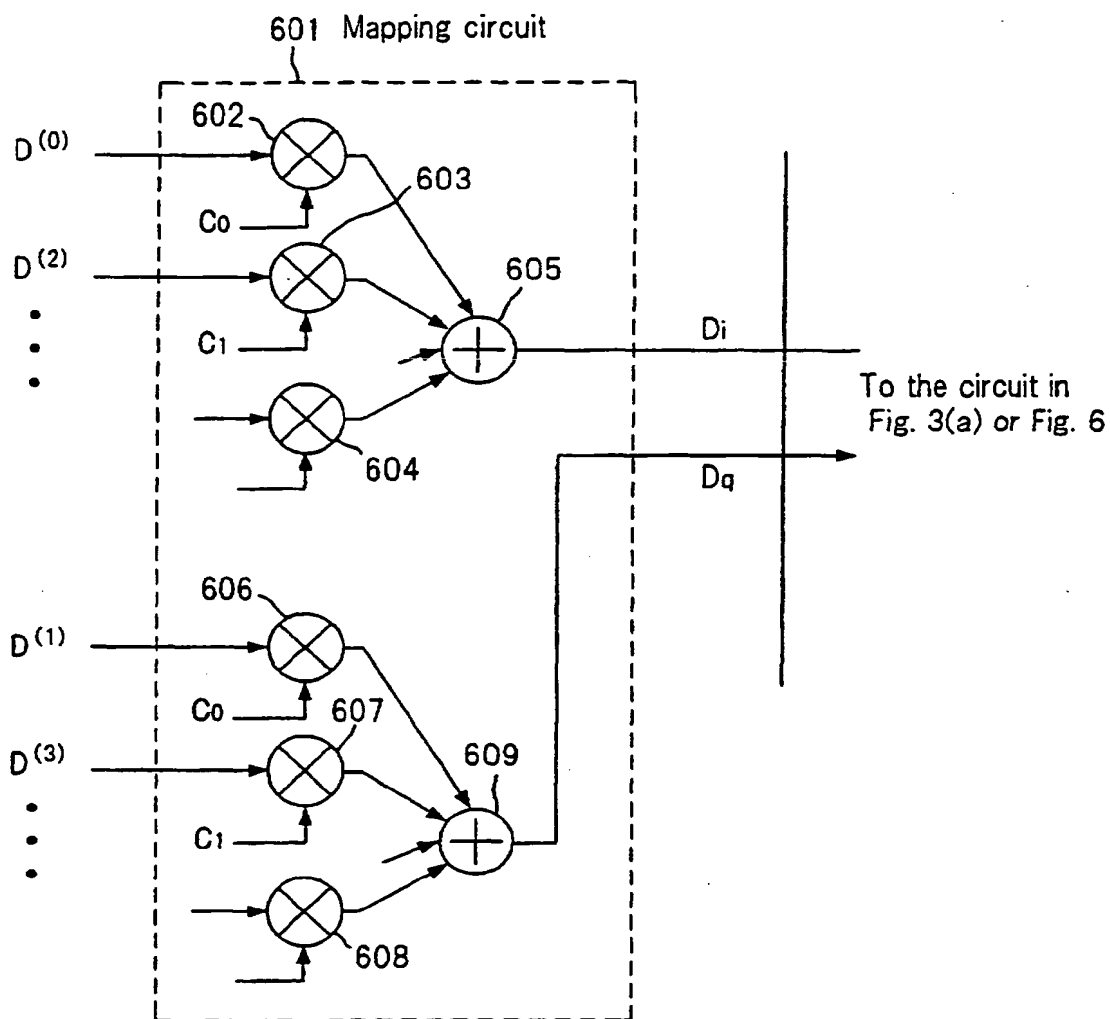


Signal points in the
first embodiment



Signal points in the
second embodiment

FIG. 8



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP00/01162 -

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl. ⁷ H04B1/707, H04J13/04		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl. ⁷ H04B1/69-1/713, H04J13/00-13/06 H04L27/18-27/233		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-2000 Kokai Jitsuyo Shinan Koho 1971-2000 Jitsuyo Shinan Toroku Koho 1996-2000		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP, 5-235894, A (Fujitsu Limited),	1-4,
A	10 September, 1993 (10.09.93), Full text; Figs. 1 to 5 (Family: none)	5-9
A	JP, 6-232838, A (Hitachi, Ltd.), 19 August, 1994 (19.08.94) (Family: none)	1-9
A	JP, 9-46271, A (NEC Corporation), 14 February, 1997 (14.02.97) (Family: none)	1-9
A	EP, 783210, A2 (NEC CORPORATION), 09 July, 1997 (09.07.97) & JP, 9-153883, A & US, 5956328, A & AU, 712195, B & KR, 97031421, A	1-9
P, A	EP, 921652, A2 (Electronics and Telecommunications Reserch Institute), 09 June, 1999 (09.06.99) & JP, 11-186989, A	1-9
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
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Date of the actual completion of the international search 22 May, 2000 (22.05.00)		Date of mailing of the international search report 06.06.00
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

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